

第 40 回工学院特別セミナー 兼 電気電子系科学技術懇話会  
第 4 回集積 Green-niX セミナーシリーズ後援  
The 40th School of Engineering, Distinguished Seminar;  
EE Forum on Science and Technology; and 4th Integrated Green-niX Seminar Series



“Forge the Future:  
Unleash Your STEM Superpowers  
in Semiconductors”



Ms. Thy Tran, Vice President of Global Frontend  
Procurement, Micron Technology, Inc.

日時：2024年3月12日(火) 10:00-11:00, Zoom 対 Time: 10:00-11:00, Tue., March 12th, 2024, Zoom  
象：本学の学生、本学の教職員 For **All STUDENTS** & all faculties/staff

**Abstract:** Hardware, software, edge vs. cloud computing, and everything under the sun – Semiconductors is where it's at! With over JPY 150 trillion in projected global semiconductor sales revenue by 2030, finding your STEM superpowers in semiconductors will unleash your potential to forge the future. The ever-increasing demand for electronics and computer systems to fuel the explosive data growth for applications such as Artificial Intelligence, High Performance Computing, Connectivity (IoT, 5G), Auto Electronics, Advanced driver-assistance system, and Autonomous Driving means that there are enormous opportunities for engineers and scientists to design, develop and create the materials and architectures in these devices. With 30 years of technical experience in the semiconductor industry, Thy Tran will share her “Refugee to VP” leadership journey and provide insights on what it takes to craft and scale semiconductor devices and products, and how semiconductor “techies” can unleash our superpowers to shape our world.

**Biography:** Thy Tran, Vice President of Global Frontend Procurement at Micron Technology, Inc., recently transitioned from her prior role as VP of DRAM Process Integration where she led a global team in the US and Asia to drive DRAM technology development and transfer into high-volume manufacturing fabs. She has nearly 30 years of semiconductor experience working in the US, Europe, and Asia, including two semiconductor fab startups. Ms. Tran joined Micron in 2008 and led several DRAM module development programs including Advanced Capacitor, Metallization, and Through-Silicon-Via (TSV) integration before taking on the DRAM Process Integration node leadership role for several generations. Her technical contribution has been integral to Micron's DRAM Technology Development Roadmap and played a significant role in helping Micron achieve DRAM technology leadership. Prior to Micron, Ms. Tran worked on Logic and SRAM technologies at Motorola, Inc. and SRAM at WaferTech, LLC. At Siemens, AG., which later spun off to Infineon and Qimonda, AG. she held several leadership roles in DRAM technology development transfer, and manufacturing. Ms. Tran graduated from the University of Washington with a Bachelor of Science in Electrical Engineering. She is a recent alumnus of the Stanford Graduate School of Business's Executive Program and the McKinsey Executive Leadership Program. She is a senior member of IEEE, member of the Society of Women Engineers, and serves as a Strategic Advisory board member for the University of Washington's Electrical and Computer Engineering department, International Semiconductor Executive Summit, Green Grid Inc., and Mercado Global. She is a recipient of Global Semiconductor Alliance's 2023 Rising Women of Influence award.

<https://us02web.zoom.us/meeting/register/tZEduupqjojGdFs5CeIo6f3h5l6AoH14kUb>

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