

■ Doctoral thesis oral defense (completion expected March 2020)

1	【Wave】 Date : Decemeber 26, 2019 Venue : Room 201, 2nd Floor, South 3rd Building, Ookayama Campus					
	Presentation time	Name	Graduate Major	Supervisor	Co-supervisor	Doctoral thesis title
	9:00 ~ 10:30	Masafumi Nagasaka	Electrical and Electronic Engineering	Jiro Hirokawa		Study of Dual-band Circularly Polarized Planar Feeds and Application to Reflector Antenna for Satellite Broadcasting Reception
1	【Device】 Date : Jan. 15th (Wed), 2020 Venue : Meeting room 1, 2nd floor in Suzukake Hall					
	Presentation time	Name	Graduate Major	Supervisor	Co-supervisor	Doctoral thesis title
	13:30 ~ 15:00	Min Gee Kim	Electrical and Electronic Engineering	Shun-ichiro Ohmi		A study on the ferroelectric nondoped HfO2 gate insulator formed by reactive sputtering for MFSFET application
2	【Device】 Date : Date : January 21st on Tuesday, 10:00-12:00 Venue : 1st/2nd meeting room, S2 building, Suzukakedai Campus					
	Presentation time	Name	Graduate Major	Supervisor	Co-supervisor	Doctoral thesis title
	10:00 ~ 12:00	Kentaro Matsuura	Electrical and Electronic Engineering	Hitoshi Wakabayashi		Chip Level Integration for High Performance Sputtered-MoS2 nMISFETs
1	【Electronic Materials】 Date : 1/10/2020 Venue : Ookayama campus, South Bldg. 3, 2nd Floor, 201					
	Presentation time	Name	Graduate Major	Supervisor	Co-supervisor	Doctoral thesis title
	10:00 ~ 12:00	Daiki Kitagata	Electrical and Electronic Engineering	Satoshi Sugahara		Study on low-power technologies for CMOS logic systems using nonvolatile/virtually-nonvolatile retention
1	【Power】 Date : January 6, 2020 Venue : W8-E1001					
	Presentation time	Name	Graduate Major	Supervisor	Co-supervisor	Doctoral thesis title
	13:20 ~ 14:50	Hadi Setiadi	Electrical and Electronic Engineering	Fujita		Study of Switched-Capacitor-based Resonant Converters for Improving Conversion Efficiency over a Wide Operating Voltage and Current Range
2	15:05 ~ 16:35	Higashi Moritaka	Electrical and Electronic Engineering	Takeuchi		Study on Detection and Evaluation of Partial Discharge for Power Cable Circuits

※Group of Circuit isn't applicable.